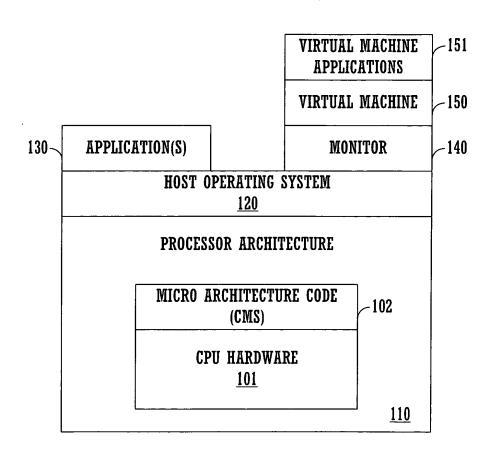
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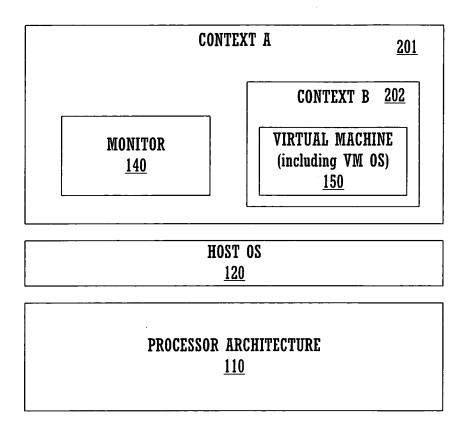


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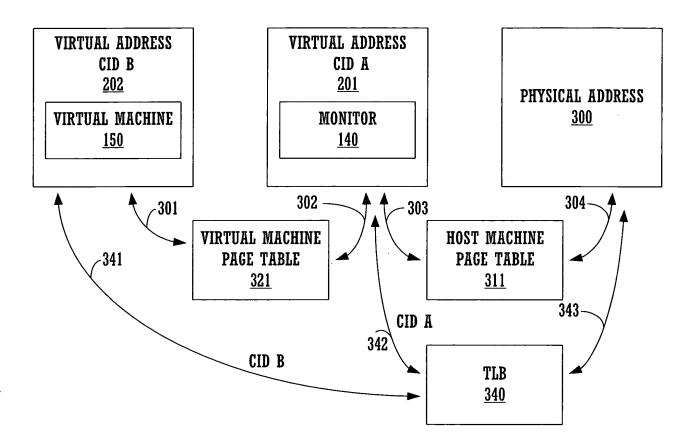
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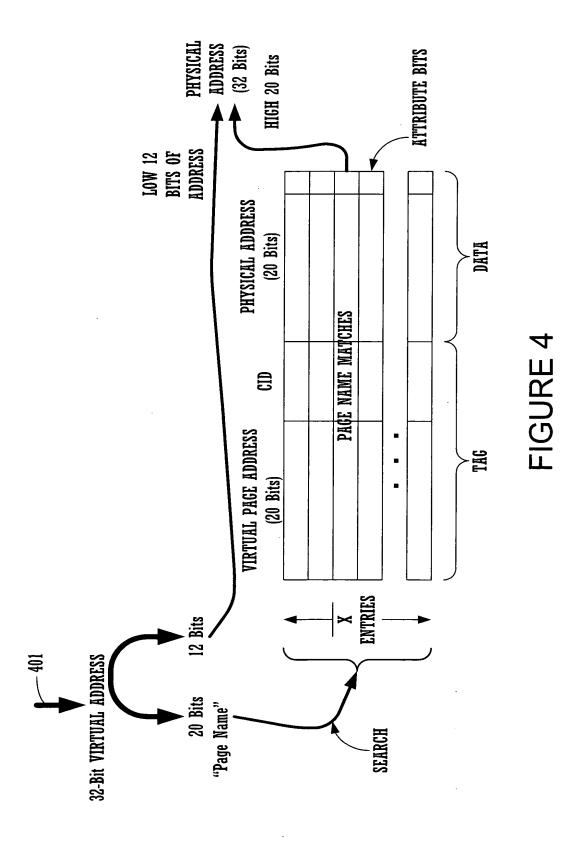
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500 INSTANTIATE AND EXECUTE A HOST MACHINE OPERATING SYSTEM IN A HOST MACHINE CONTEXT 501 INSTANTIATE AND EXECUTE A VIRTUAL MACHINE OPERATING SYSTEM IN A VIRTUAL MACHINE CONTEXT <u>502</u> STORE A PLURALITY OF TLB ENTRIES FOR THE VIRTUAL MACHINE CONTEXT AND THE HOST MACHINE CONTEXT WITHIN THE TLB 503 MAINTAIN A HOST MACHINE VIRTUAL MEMORY ADDRESS SPACE USING A HOST MACHINE PAGE TABLE <u>504</u> MAINTAIN A VIRTUAL MACHINE VIRTUAL MEMORY ADDRESS SPACE USING A VIRTUAL MACHINE PAGE TABLE 505 EXECUTE ONE OR MORE VIRTUAL MACHINE APPLICATIONS AND PROVIDE APPLICATION FUNCTIONALITY 506 VIRTUAL MACHINE NO **CONTEXT TLB Miss?** 507 **↓** YES WALK THE VIRTUAL MACHINE PAGE TABLE TO OBTAIN THE **CORRESPONDING HOST MACHINE VIRTUAL ADDRESS** 508 HOST MACHINE NO **CONTEXT TLB Miss?** <u>509</u> **₩YES** WALK THE HOST MACHINE PAGE TABLE TO OBTAIN THE **CORRESPONDING PHYSICAL ADDRESS** 510

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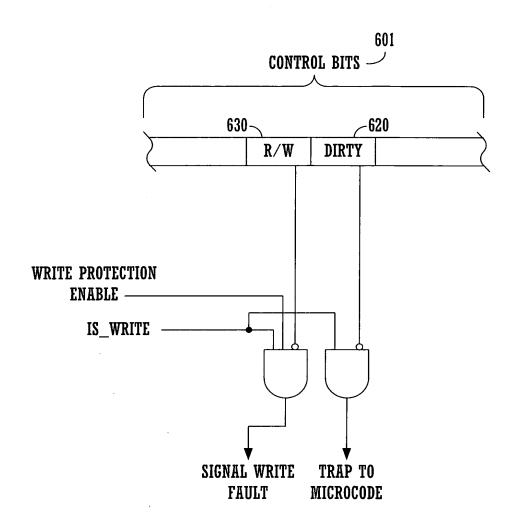
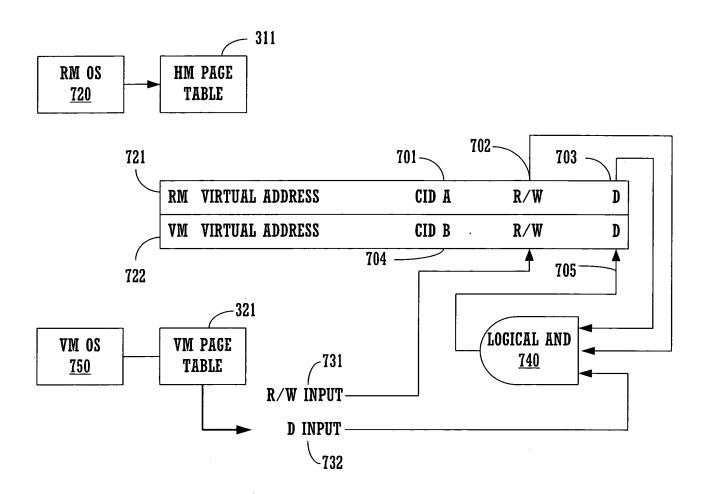


FIGURE 6

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<u>800</u>

INSTANTIATE AND EXECUTE A HOST MACHINE OPERATING SYSTEM IN A HOST MACHINE CONTEXT 801

INSTANTIATE AND EXECUTE A VIRTUAL MACHINE OPERATING SYSTEM IN A VIRTUAL MACHINE CONTEXT 802

STORE A PLURALITY OF TLB ENTRIES FOR THE VIRTUAL MACHINE CONTEXT AND THE HOST MACHINE CONTEXT WITHIN THE TLB

803

ACCESS MEMORY PROTECTION BITS FOR TLB ENTRIES FROM THE HOST MACHINE OPERATING SYSTEM AND LOGICALLY COMBINE THEM TO GENERATE MEMORY PROTECTION BITS FOR TLB ENTRIES FOR HE VIRTUAL MACHINE OPERATING SYSTEM

804

ENFORCE MEMORY PROTECTION ON THE VIRTUAL MACHINE OPERATING SYSTEM USING THE GENERATED MEMORY PROTECTION BITS FOR THE TLB ENTRIES 805

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